

Notice of References Cited	Application/Control No. 10/783,816		Applicant(s)/Patent Under Reexamination CISCHKE, CHRISTOPHER M.	
	Examiner Russ Guill		Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,446,241	09-2002	Mobley et al.	716/4
*	B	US-2001/0001157	05-2001	Oura, Hironobu	714/42
*	C	US-5,386,579	01-1995	Bourekas et al.	712/43
*	D	US-2004/0181655	09-2004	Azuma, Tetsuhiko	712/241
*	E	US-2005/0060132	03-2005	Hollander et al.	703/014
*	F	US-6,968,520	11-2005	Kawabe et al.	716/5
*	G	US-6,182,258	01-2001	Hollander, Yoav	714/739
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	David A. Wood et al.; "Verifying a multiprocessor cache controller using random test generation", August 1990, IEEE Design and Test of Computers, Volume 7, Issue 4, pages 13 - 25
	V	Donald E. Knuth, "The Art of Computer Programming", Volume 2/Seminumerical Algorithms, Third Edition, 1998, Addison Wesley Longman, pages 26 - 28
	W	A. Chandra et al.; "AVPGEN - A Test Generator for Architecture Verification", June 1995, IEEE Transactions on Very Large Scale Integration Systems, Volume 3, Number 2, pages 188 - 200
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.